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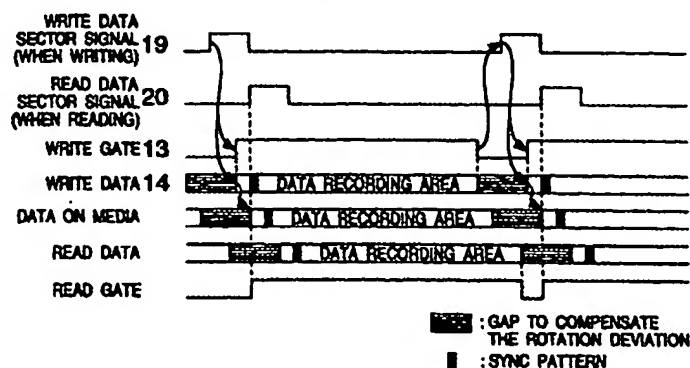
(54) METHOD AND DEVICE FOR RECORDING AND REPRODUCING DATA

(57) The substantial data recording area on the recording medium is expanded by reducing the gap area to compensate the delay time of data conversion while data recording and reproducing in data storage and retrieval device. Namely, the purpose is the improvement of the efficiency of formatting.

Based on servo sector signal 10, data sector generator circuit 16 generates write data sector signal 19, which indicates the timing of data recording in data recording area, and transmits the data to delay circuit

17 and selector 18 which has 2 input port and 1 output port. Delay circuit 17 outputs read data sector signal which is a delayed signal of write data sector signal with certain period. These 2 signals are inputted to selector 18. Selector 18 outputs write data sector signal 19 when write mode signal 21 is active, and outputs read data sector signal 20 as data sector signal 15 when write mode signal is inactive.

FIG. 5



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Description

TECHNICAL FIELD

[0001] This invention is related to data storage and retrieval device which utilizes magnetic disk, magneto optical disk, optical disk, and magnetic tape etc. Especially this invention is related to data storage and retrieval control method in which inter sector gap area is reduced while data recording and reproducing, and also related to device using it.

BACKGROUND ART

[0002] Data recording and reproducing method of conventional data storage and retrieval device is disclosed with an example of magnetic disk device. However the present invention is effective not only in magnetic disk device but also various data storage and retrieval devices in which a train of data is separated to one or more blocks and recorded to the data recording medium with arranging a gap before or after each sector, and data reproducing is performed through a reproducing head which faces and moves against said recording medium.

[0003] In a magnetic disk device, concentrically shaped recording area called track is arranged on a magnetic disk namely the recording medium, and data recording and reproducing is performed by the information on the track. Servo area is arranged intermittently and periodically on this track, and data area is also arranged between the servo area. Said data recording area usually consists of plural of data sectors. Between the sectors, there are gap area which compensates the rotational speed deviation or delay time of the operation of electronic circuit. Accordingly data cannot be recorded here.

[0004] Each sector consists of ID portion and data portion. ID portion consists of synchronizing pattern to distinguish data while data reproducing, ID information to identify the data sector, and error check codes to detect the ID information error. Data portion consists of synchronizing pattern to distinguish data while data reproducing, data information, and error correction code (ECC) etc.

[0005] Recording and reproducing circuit which performs recording and reproducing of data on said data portion consists of read circuit, write circuit, and data control circuit. Read circuit handles reproducing of signal from ID portion and data portion. Write circuit handles recording the signal to data portion. Based on the servo sector signal of said servo area, data control circuit generates data sector signal which is activated at the start point of synchronizing pattern of ID portion, and controls overall recording and reproducing operation of the device.

[0006] Data recording procedure is as follows; ID data is reproduced with referring to Data sector signal as the

origin, it is confirmed to be the correct sector to be recorded, and write data is recorded by write circuit. Data reproducing procedure is as follows; ID data is reproduced, it is confirmed to be the correct sector to be reproduced, the following data area is read back by read circuit, and the desired data of the sector is obtained.

[0007] Further, detail procedure is described as follows. Read gate signal is activated by the data control circuit at the appropriate timing during the synchronizing pattern area of the ID portion with referring to the data sector signal as the origin. Read circuit synchronizes with the signal recorded in ID portion, decodes the reproduced signal to data, gets the ID information, and transmits the ID data to data control part. Data control circuit verifies the error check code of ID information and temporarily closes the read gate, then reactivates the read gate signal at an appropriate timing during the synchronizing pattern of the data portion after ID portion. Read circuit synchronizes with data portion, decodes the reproduced signal, and transmits the reproduces data to data control circuit. After decoding error correction code portion (ECC) of data portion, data control circuit closes the read gate, and shifts to a status of waiting the data sector signal for decoding next ID portion.

[0008] In a series of data recording and reproducing operation, gap areas are arranged between data sectors, or before and after ID portion and data portion, in order to compensate the deviation of recording position which is caused from the rotational speed deviation of magnetic disk which is the recording medium in magnetic disk device, or the deviation of tape running speed in magnetic tape drive, and to compensate the delay in data recording operation of write circuit, or the delay in data reproducing operation of read circuit.

[0009] In most cases, the rotation of magnetic disk is controlled based on a clock signal generated by a crystal oscillator, so that usually the rotational speed is stable. However when the device is exposed to a shock or vibration, or when seek operation occurs, the rotational speed of the disk varies because of the deviation of the power voltage or mechanical resonance from seek operation. In such cases the amount of the deviation of the rotational speed is about 0.1 %. Above mentioned gap areas were necessary to avoid the data destruction from the deviation of the rotational speed.

[0010] Delay time in operation of write or read circuit varies according to the data conversion method (coding format) or the reproducing method of reproducing the data recorded on the data storage medium (here magnetic disk). As for the coding method, recently more efficient block coding method such as 8 to 9 coding method or 16 to 17 coding method is generally used instead of formerly used 2 to 7 RLL or 1 to 7 RLL. As for the reproducing method, PRML method (Partial Response Maximum-Likelihood) or EPRML (Extended Partial Response Maximum-Likelihood) is generally used in order to obtain the better signal to noise ratio. With the

Increasing complexity of the data conversion method or signal reproducing method, coding or decoding time increases, accordingly the operation time (delay time in data recording or reproducing) required the reproduced data to reach to data control circuit from read circuit increases. Now data control circuit cannot start the operation of next data portion or ID portion until ECC decoding operation of the reproduced data finishes. So gap area to wait the delay time of write or read circuit was necessary before and after the data portion or ID portion.

[0011] Track format in the conventional magnetic disk device is shown in Fig. 7. Recording and reproducing operation of data is performed depending on one data sector signal (for write and read) as timing signal. Servo area and SYNC pattern, those are necessary to record and reproduce data on data area, data recording area, gap area to compensate rotational speed deviation, and gap area to compensate the operation time (data conversion delay time) of write or read circuit are intermixedly arranged on the track. And there are time delay each between write data to be recorded on the recording medium, actually recorded data on the medium, and reproduced data respectively.

[0012] A method to delay the output of index signal and sector signal except formatting in order to avoid write splicing is disclosed in unexamined patent publication 5-303836 Japan. However only the timing of generating the sector signal is changed between formatting and not formatting, but there is no effect to reduce above mentioned gap area (namely the area cannot be used to store data).

[0013] In a magnetic disk device by the prior technology, starting of write or read operation of data is triggered by the data sector signal generated from servo signal. The data sector signal was generated at the same timing for both writing and reading data. In such above mentioned prior control method of data recording and reproducing, said gap area is determined by the total of the deviation of relative speed (rotational) of the medium, the delay in write operation, and the delay in read operation. Originally the gap area cannot be used for data recording. Accordingly by increasing the gap area, the actual data recording area decreases.

DISCLOSURE OF THE INVENTION

[0014] The purpose of the present invention is to eliminate the portion of gap area which is useless to store the data, so that to increase the substantial data recording area of the recording medium. Another purpose of the present invention is to reduce the portion of gap area with which the delay time of read or write operation is compensated, so that the efficiency of the data recording is improved.

[0015] In the data recording and reproducing method by the present invention, to achieve the above mentioned object, data sector signal generating means is

provided where the timing of generating the data sector signal of data recording and reproducing is different. Here data sector signal is generated for each sector. Said data sector signal generating method can issue the two kinds of data sector signal either by changing the timing from on source signal or from pre-arranged two source signal of the different timing.

[0016] Further, in order to reduce the gap portion which compensates the delay time of read operation, it is provided with data sector generating means which generates data sector signal to start the data reading of the next sector before completing the data output operation of the current sector, and data reproducing means.

[0017] Or, it may be provided with data sector generating means which starts synchronizing with sync pattern and reproducing, before completing the data output operation of the current sector, and data reproducing means.

[0018] In order to reduce the gap portion which compensates the delay time of write operation, it is provided with data sector generating means which generates data sector signal to start the data writing for the next sector before completing the data output of current sector. Consequently, the gap area is substantially eliminated, and the data recording efficiency on the recording medium can be improved.

BRIEF DESCRIPTION OF THE DRAWINGS

[0019]

Fig. 1 is a block diagram of a magnetic disk device by the present invention.

Fig. 2 is a block diagram which shows a configuration of data sector generator 6 in Fig. 1.

Fig. 3 is a block diagram which shows a configuration of the read circuit 3 in Fig. 1.

Fig. 4 is a block diagram which shows a configuration of the write circuit 4 in Fig. 1.

Fig. 5 shows the timing of reading and writing in the present invention.

Fig. 6 shows the timing of the read operation by the present invention.

Fig. 7 shows the timing of reading and writing in the prior technology.

BEST MODE FOR CARRYING OUT THE INVENTION

[0020] Fig. 1 is a block diagram of a magnetic disk device which is an embodiment of the present invention. Reproducing and recording operation is disclosed.

[0021] Data reproducing operation is as follows. Data and servo information stored in the data disk 1 is read back as analog read signal 8 (head play back signal 8) and is inputted to read circuit 3 and servo sector generator 5.

[0022] Based on the servo information in read back signal 8, Servo sector generator 5 outputs servo sector

signal 10 which shows start point of servo information recording area to data sector generator 6. Following servo sector signal 10, data sector generator 6 outputs data sector signal 15 (digital signal) which indicates the base timing for recording and reproducing to and from the data recording area. Data sector signal 15 is inputted to data control circuit 7, and based on data sector signal 15, data control circuit 7 outputs read gate 11 which is a digital signal and determines the area to import the data. While read gate 11 is active, read circuit 3 decodes reproduced signal 8 and output to data control circuit 7 as read data 12.

[0023] Here, trigger signal 10 which indicates the origin of any recording area can be issued by trigger signal generator 5 (not shown) to data generator 6 (not shown), and origin trigger signal 15 can be issued to data control circuit 7.

[0024] Then, the procedure of data recording operation is disclosed. Data recording procedure performed based on data sector signal 15 like reproducing procedure. Above mentioned and not shown origin trigger signal 15 can also be used.

[0025] Data control circuit 7 issues write gate 13 which is digital and determines the data recording area based on the data sector signal 15 which is issued from data sector generator 6. Further, data control circuit 7 outputs write data 14 to write circuit 4 while write gate 13 is active. Write circuit 4 receives write gate 13 and write data 14, encodes write data 14, and records analog converted write data 9 to data disk 1 through magnetic head.

[0026] Data sector generator 6 in the functional block diagram Fig. 1 is further described referring to Fig. 2.

[0027] Data sector generator 16 generates write data sector signal 19 based on the input of servo sector signal 10. Write data sector signal 19 indicates the timing of recording the data to data recording area on the medium.

[0028] Write data sector signal 19 is transmitted to delay circuit 17 and selector 18 which has 2 input port and 1 output port. Delay circuit 17 outputs read data sector signal 20 which is a certain period delayed signal of write data sector signal 19. These two signals are inputted to selector 18. Selector 18 outputs write data sector signal 19 as data sector signal 15 when write mode signal 21 is active, and outputs read data sector signal 20 as data sector signal 15 when write mode signal 14 is inactive.

[0029] Further, the circuit configuration is not limited as above. That means, only generating each signal as shown in Fig. 5 is necessary. One way is to provide the data control circuit with above mentioned function, and to transmit the write data sector signal in whether delayed of certain period or write data sector signal 19 itself depending on the existence or no-existence of write mode signal 21, in either synchronously or a synchronously with a clock which is generated separately. Another way is to provide an additional control circuit to

perform the same function.

[0030] Here, timing of the procedure of write and read operation is shown in Fig. 5.

[0031] When a magnetic disk device performs write operation, write mode signal 21 (Fig. 2) is active and data sector signal 15 indicates the timing signal to write data sector signal 19. Data control circuit 7 outputs write gate 13 and write data 14 based on data sector signal 15 (write data sector signal 19), and the information is written on to the data disk 1. Because of the operation delay of write circuit 4, recording position lag occurs on data disk 1. Accordingly the write data sector signal 19 is asserted earlier with the delay time of write circuit 4. The following write data sector signal 19 is re-asserted after transmitting the write data and passing the gap period to compensate the deviation of rotation. According to these means, the gap area includes only the portion to compensate the deviation of rotation, and no portion for the delay of prior write operation is included in the gap area.

[0032] On the other hand when a magnetic disk performs read operation, write mode signal 21 is inactive, and data sector signal 15 is the timing signal indicated to read data sector signal 20. Read data sector signal 20 is asserted at delayed timing with write data sector signal 19, and is generated at the start position of data recording area of the medium (Fig. 5). Read data 12 is transmitted to data control circuit 7 after the delayed operation of read circuit 3.

[0033] In case delay time of read operation is less than the time of gap area to compensate the rotational deviation, namely less than the time for the magnetic head passes said gap area, the delay time of read operation is covered up. Consequently the gap area is not required to include the delay time of read operation, so that the gap area can be reduced compared to the prior technology.

[0034] Further the operation time after the issue of read command until the actual start of read operation of the data on the medium, or operation time to store the data can be compensated similarly. To be more detail, the gap area on the medium can be eliminated by anticipation control of the data control circuit with reflecting the time after starting the input of reproduced signal 8 to read circuit 3 and servo sector generator 5 until it is converted to the information which the data control circuit can distinguish, or the time after starting the transmit of the write data 14 to write circuit 4 until the starting of recording the magnetic field change which corresponds to the write analogue signal to the recording medium.

[0035] With another method than data sector generation circuit 16 of the present embodiment, it is possible to achieve the similar object. For example, similar function can be obtained by providing two kind of circuit in data sector generation circuit 16, that is write data sector generation circuit and read data sector generation circuit, write data sector signal and read data sector signal are generated, and outputting one of the two signals

as the data sector signal. The only required is that the timing between each signal is as shown in Fig. 5.

[0036] Then, when the read operation delay time is larger than the time which the magnetic head passes the corresponding gap area, an operational circuit is required as shown in Fig. 3. More detailed configuration of read circuit 3 and data control circuit 7 in Fig. 1 is shown in Fig. 3. Read circuit 3 consists of analog decoder 22, read data synchronous control circuit 23 and data conversion circuit 24.

[0037] Operational timing is shown in Fig. 6.

[0038] During read operation, based on data sector signal 15, data control circuit 7 generates sync control start signal 25 from either the fall of write sector signal or the rise of read delay selector signal, and also generates read gate 11 with the delayed timing of the rise of write sector signal. Sync control start signal 25 (Fig. 3) is transmitted to analog decoder circuit 22 and read sync control circuit 23. Read gate 11 is transmitted to data conversion circuit 24. When sync control start signal 25 is activated, reproduced analog signal 8 is decoded through analog decoder circuit 22.

[0039] Decoded data is inputted to data conversion circuit 24 and read data sync control circuit 23. Read data sync control circuit 23 performs phase synchronizing of data with the sync pattern of reproduced signal 8, and generates the reproducing clock signal. Decoded data is inputted to data conversion circuit 24 with synchronizing the reproducing clock signal, and the data code is converted and outputted to data control circuit 7 as read data 12.

[0040] Data control circuit 7 inactivates the sync control start signal 25 (Fig. 6) when analog decoder circuit 22 finishes the processing of reproduced signal 8, and inactivates read gate 11 when the output of all read data 12 is finished. After all, in case reproducing operation is performed continuously, sync control start signal 25 is inactivated when processing of all the data in one sector by analog decoder circuit is completed. Accordingly even though data transfer to data control circuit 7 is not completed yet, by starting to wait the next sync control start signal at the timing of sync control start signal 25 is inactivated, the data decoding and sync control can be started when sync control signal 25 is activated by next data sector signal 15.

[0041] By employing the present invention, even if the delay time of read operation exceeds the time of gap area, the gap area does not include the portion of read operation, so that the purpose of the invention is achieved.

[0042] Further, the circuit configuration is not limited as above. That means, only generating each signal as shown in Fig. 6 is necessary. One way is to provide the data control circuit with above mentioned function, and another is to provide a separate control circuit to perform the same function.

[0043] Next, in Fig. 4 another embodiment of a circuit is disclosed where said gap area caused from the delay

of write circuit is reduced.

[0044] In Fig. 4, gate generation circuit 26 is provided to write circuit 4 of Fig. 1. From write gate signal 13 (Fig. 5) which indicates the outputting period of digital data 14, gate generation circuit 26 generates write gate with reflecting the delay time of data code conversion of write data 14. That is, though the write gate signal 13 is closed at completion of the transmitting of the write data 14 from data control circuit 7 to write circuit 4, gate generation circuit 26 keeps write gate depending on write gate signal 13 until the completion of the recording to the recording medium (disk). Accordingly, write circuit 4 can continue to transmit coded recording data 9 (analog signal) to magnetic head.

[0045] In the present embodiment, it is described referring to magnetic disk device. However, gap area reduction can be obtained by similar function not only in magnetic disk devices but also in optical disk devices or magnetic tape devices.

[0046] The method by the present invention can be applied to not only data recording and reproducing devices but also the components which composes the relevant devices such as LSIs of data recording and reproducing circuits or control circuits. Further, similar function can also be achieved by micro CPU.

[0047] In the data recording and reproducing method by the present invention, timing compensation is introduced to relatively cancel the delay time in write and read operation, so that the gap portion to compensate the delay time of data processing can be eliminated or reduced. Consequently, improvement of the efficiency or data recording, namely the expansion of the substantial data recording area on the recording medium can be obtained.

Claims

1. Data recording and reproducing device, which comprises

data recording means through which data is separated into multiple blocks, and each block is recorded on the recording medium with gap area before or after said block,

data reproducing means, where data is reproduced through reproducing head which faces and moves against said recording medium, and which utilizes decoding means which decodes reproduced data from said recording medium and output means of code converted read data; characterized in that,

the time in which said reproducing head moves said gap area is shorter than the time from the start of said data decoding until the start of said read data output.

2. Data recording and reproducing method, which comprises

- step 1 which reproducing head faces recording medium on which multiple blocks of recorded data are recorded and gap areas are arranged before or after said block,
- step 2 where decoding of data from the recording medium is started,
- step 3 where reproduced data, of which said conversion is finished, is outputted; characterized in that,
- the time difference between processing of said step 2 and said step 3 is larger than the time of said reproducing head passes a gap which is arranged at before or after said block.
3. Data recording and reproducing device, which comprises
- data recording means through which data is separated into multiple blocks, and each block is recorded on the recording medium with gap area before or after said block,
- data reproducing means, where data is reproduced through reproducing head which faces and moves against said recording medium, and which utilizes decoding means which decodes reproduced data from said recording medium and output means of code converted read data; characterized in that,
- before finishing the output of the read data from the first block, the decoding of the data from the second block is started, when consecutive two blocks are called the first block and the second block.
4. Data recording and reproducing method, which comprises
- step 1 which reproducing head faces recording medium on which multiple blocks of recorded data are recorded and gap areas are arranged before or after said block,
- step 2 where decoding of data from the recording medium is started,
- step 3 where reproduced data, of which said conversion is finished, is outputted,
- step 4 where decoding the data from the recording medium is finished,
- step 5 where outputting of code converted read data is finished; characterized in that,
- said step 5, where outputting of read data from the first block is finished, is performed after said step 2 where decoding of data from the second block is started.
5. Data recording and reproducing device, which comprises
- data recording means through which data is separated into multiple blocks, and after relevant data is conversion processed by the write circuit, each block is recorded on the recording medium with gap area before or after said block,
- data reproducing means, where data is reproduced through reproducing head which faces and moves against said recording medium, and which utilizes decoding means which decodes reproduced data from said recording medium and output means of code converted read data; characterized in that,
- the timing of starting said write circuit for data recording, is earlier than the timing of starting said read data output means for data reproducing.
6. Data recording and reproducing method, which comprises
- step 1 which reproducing head faces recording medium on which multiple blocks of recorded data are recorded and gap areas are arranged before or after said block,
- step 2 where decoding of data from the recording medium is started,
- step 3 where write circuit, which processes the conversion of said data, is started for data recording on the recording medium; characterized in that,
- the timing of executing said step 2 is later than the timing of executing said step 3 when data recording or reproducing data of one block.
7. Data recording and reproducing device, which comprises
- data recording means through which data is separated into multiple blocks, and after relevant data is processed by the write circuit, each block is recorded on the recording medium with gap area before or after said block based on the write gate signal generated by the write gate generating circuit,
- data reproducing means, where data is reproduced through reproducing head which faces and moves against said recording medium, and which utilizes decoding means which decodes reproduced data from said recording medium and output means of code converted read data; characterized in that,
- during data recording, even when said write circuit starting signal is terminated, said write gate generation circuit continues to operate, so that data is recorded on said recording medium based on said write gate signal.
8. Data recording and reproducing method, which

comprises

step 1 which reproducing head faces recording
medium on which multiple blocks of recorded
data are recorded and gap areas are arranged 5
before or after said block,
step 2 where write circuit, which processes the
conversion of relevant data, is started and
closed for data recording on the recording
medium; characterized in that, 10
the operation of said write circuit continues until
the completion of recording said conversion
processed data to said block.

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FIG. 1

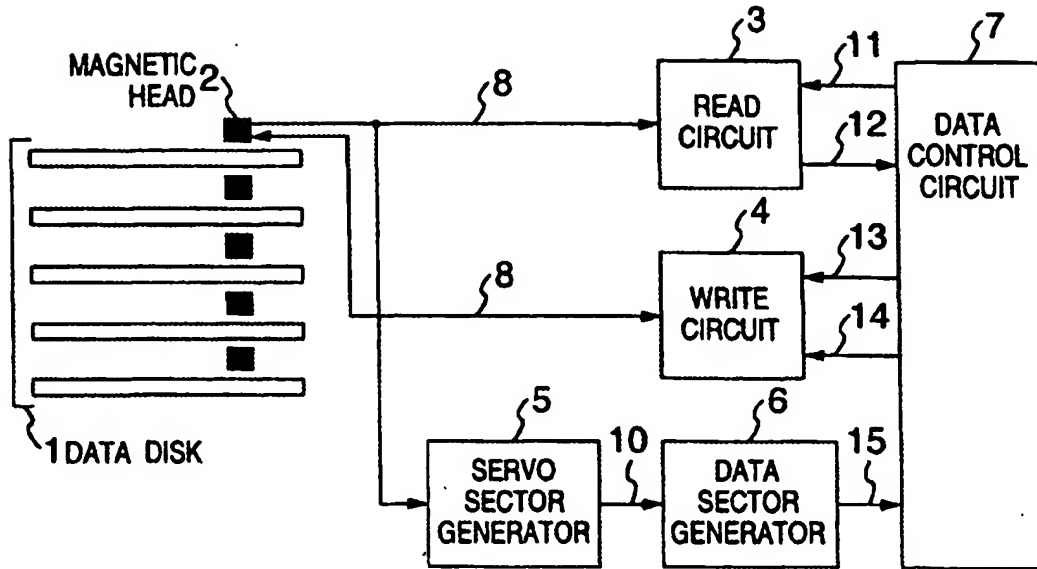


FIG. 2

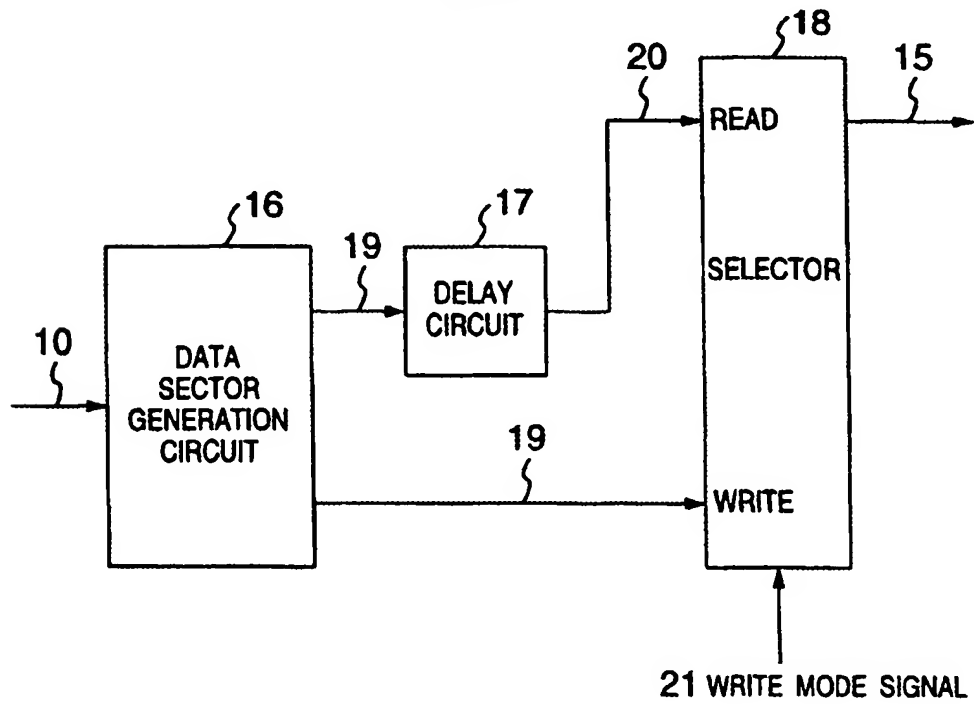


FIG. 3

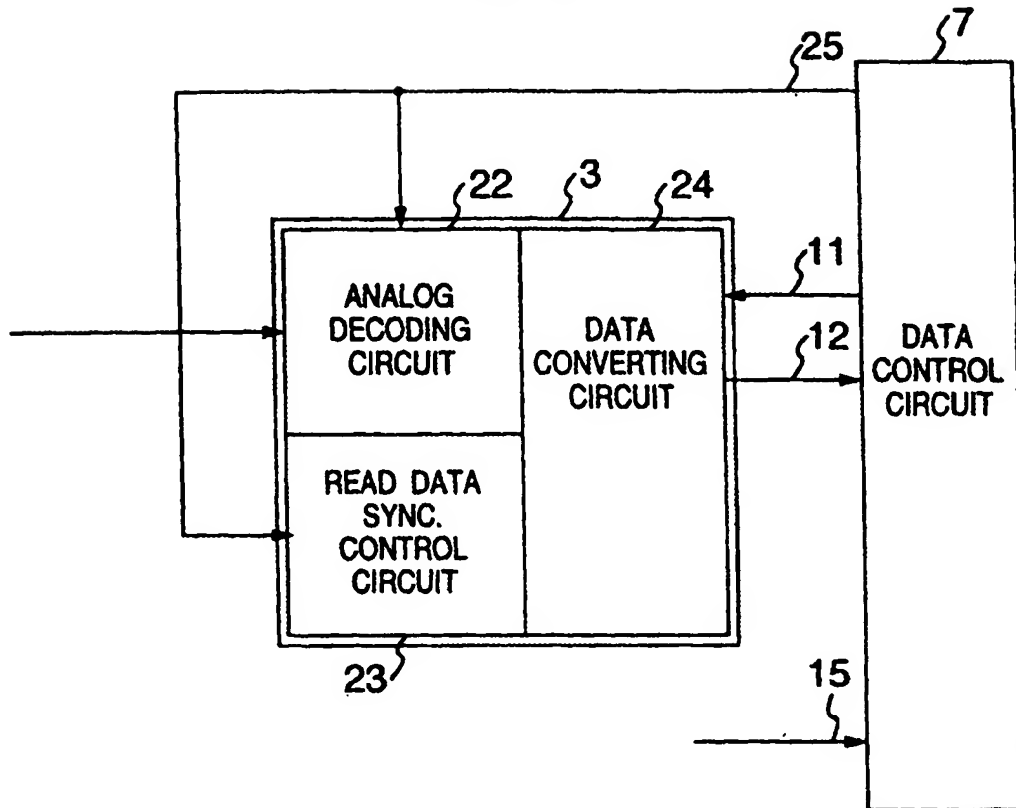


FIG. 4

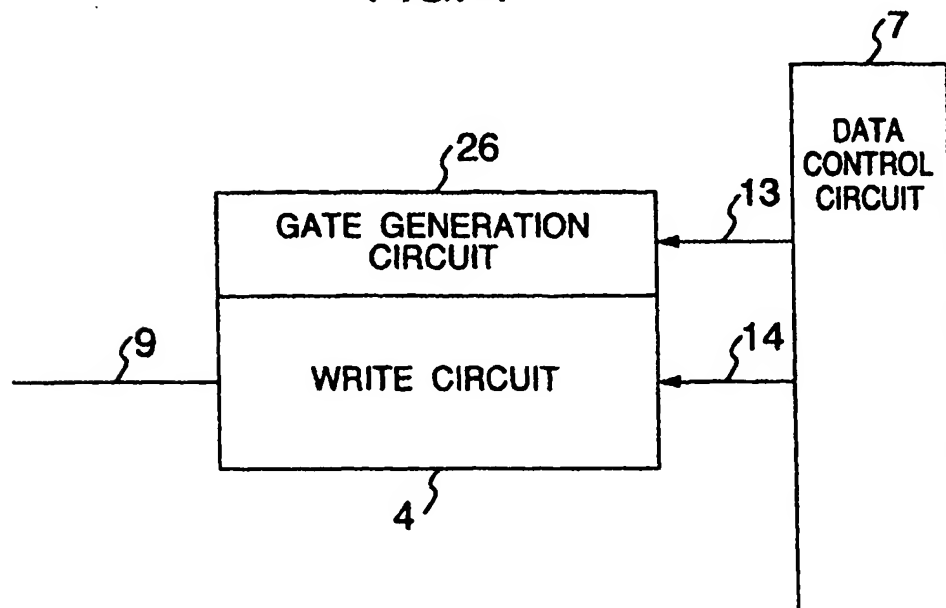


FIG. 5

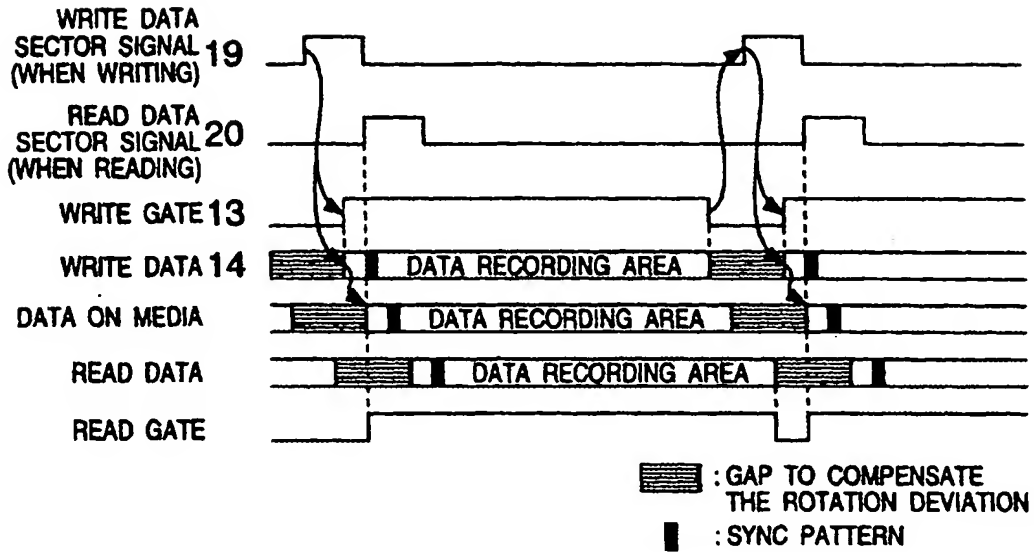


FIG. 6

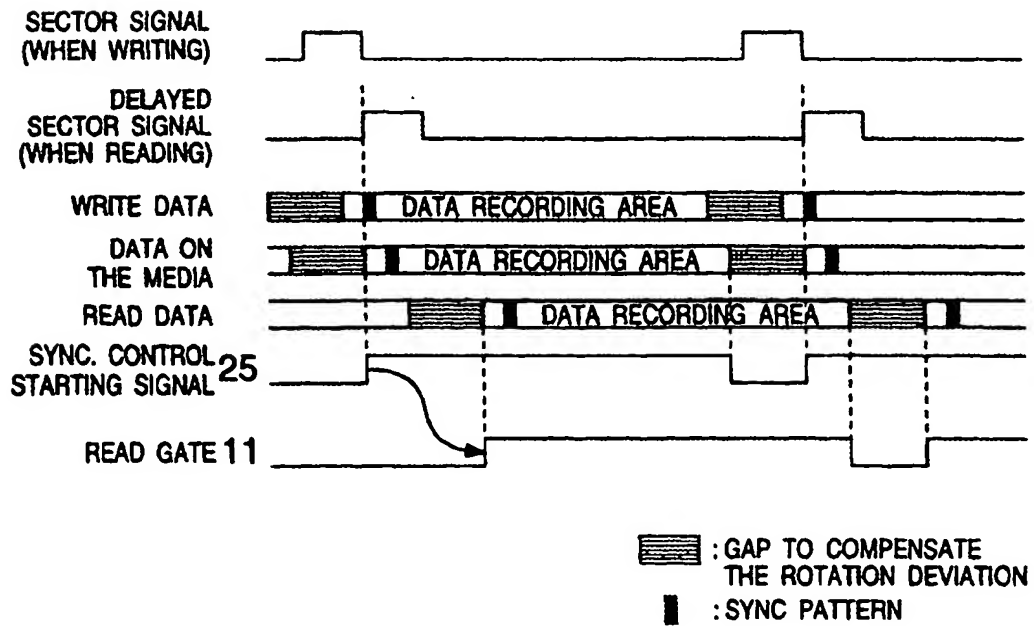
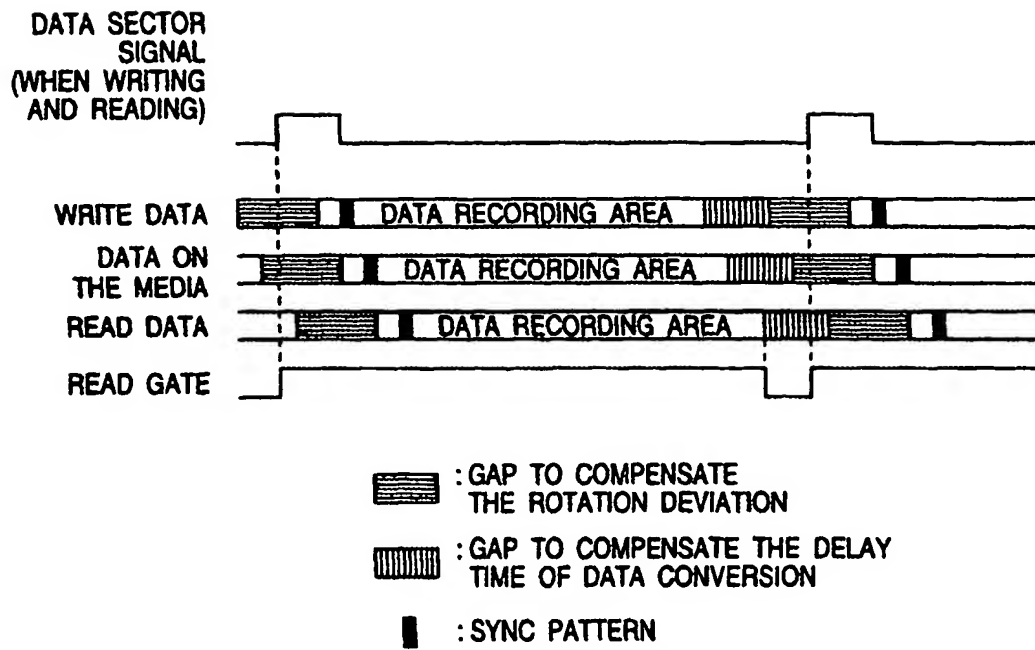


FIG. 7



INTERNATIONAL SEARCH REPORT

International application No.
PCT/JP98/01680

A. CLASSIFICATION OF SUBJECT MATTER Int.Cl ¹ G11B20/10, G11B20/12		
According to International Patent Classification (IPC) or to both national classification and IPC		
B. FIELDS SEARCHED Minimum documentation searched (classification system followed by classification symbols) Int.Cl ¹ G11B20/10, G11B20/12		
Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched Jitsuyo Shinan Koho 1926-1997 Toroku Jitsuyo Shinan Koho 1994-1997 Kokai Jitsuyo Shinan Koho 1971-1997		
Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)		
C. DOCUMENTS CONSIDERED TO BE RELEVANT		
Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	JP, 5-12805, A (Fujitsu Ltd.), January 22, 1993 (22. 01. 93) (Family: none)	1-8
A	JP, 5-120802, A (Fujitsu Ltd.), May 18, 1993 (18. 05. 93) (Family: none)	1-8
A	JP, 61-206988, A (Hitachi, Ltd.), September 13, 1986 (13. 09. 86) (Family: none)	1-8
A	GB, 1495459, A (Siemens AG.), December 21, 1997 (21. 12. 97), Page 1, line 61 to page 2, line 44 ;Page 3, line 77 to page 4, line 55 & DE, 2415366, A & AT, 349246, B & BE, 827363, A & CH, 579312, A & FR, 2266256, A & NL, 7503629, A	1-8
A	JP, 59-24408, A (NEC Corp.), February 8, 1984 (08. 02. 84) (Family: none)	1, 3, 5, 7
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Date of the actual completion of the international search July 16, 1998 (16. 07. 98)		Date of mailing of the international search report July 21, 1998 (21. 07. 98)
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